

Fig.2 (Related Art)

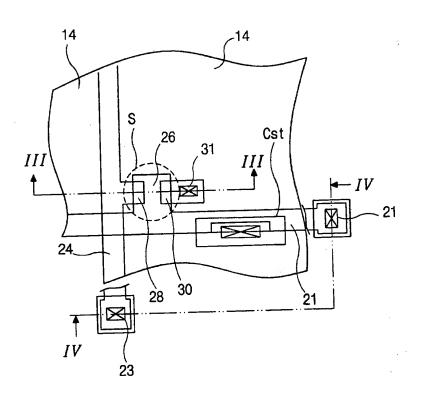


Fig. 3A (Related Art)

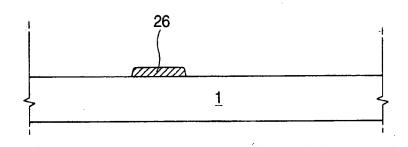


Fig.3B (Related Art)

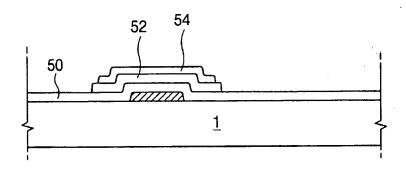


Fig. 3C (Related Art)

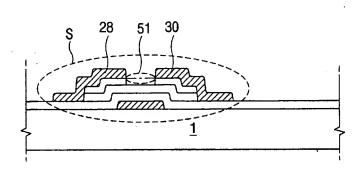


Fig. 3D (Related Art)

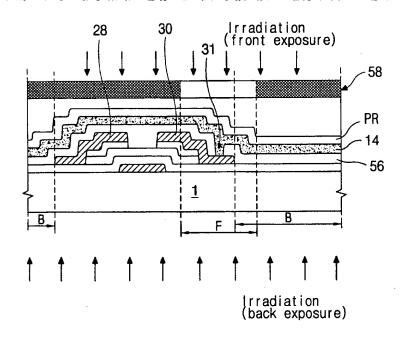


Fig. 3E (Related Art)

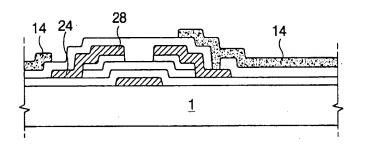


Fig. 4 (Related Art)

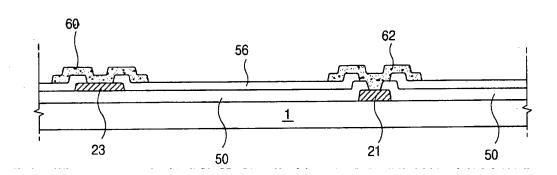


Fig. 5 (Related Art) -ST200 Cleaning substrate -ST210 Patterning metal layer -ST220 Forming gate insulation layer and semionductor layers - ST230 Forming source and drain electrodes -ST240 Forming channel region -ST250 Forming protection layer - ST260 Forming pixel electrode

Fig.6

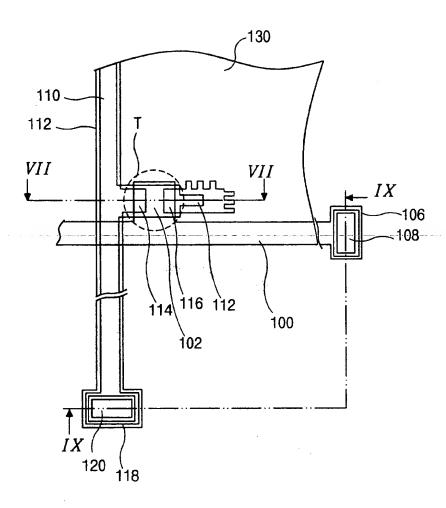


Fig.7A

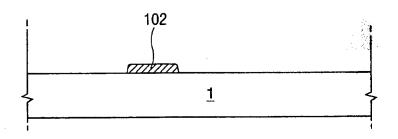


Fig. 7B

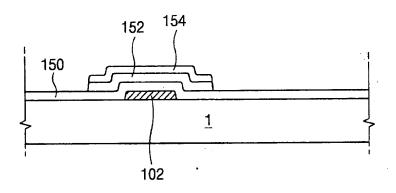


Fig 7C

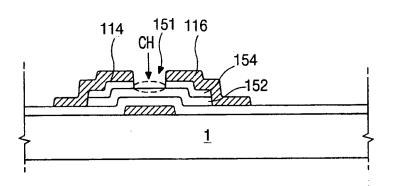


Fig .7D

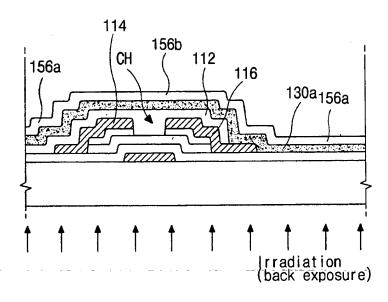


Fig. 7E

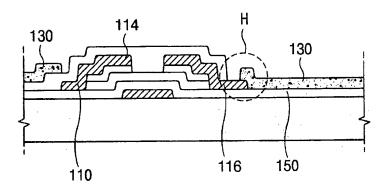


Fig. 8

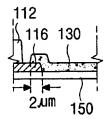


Fig. 9

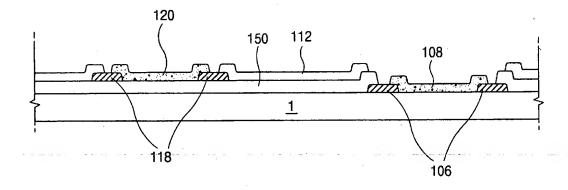


Fig. 10

